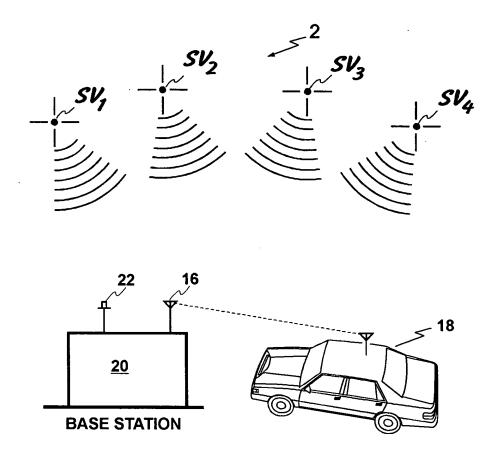
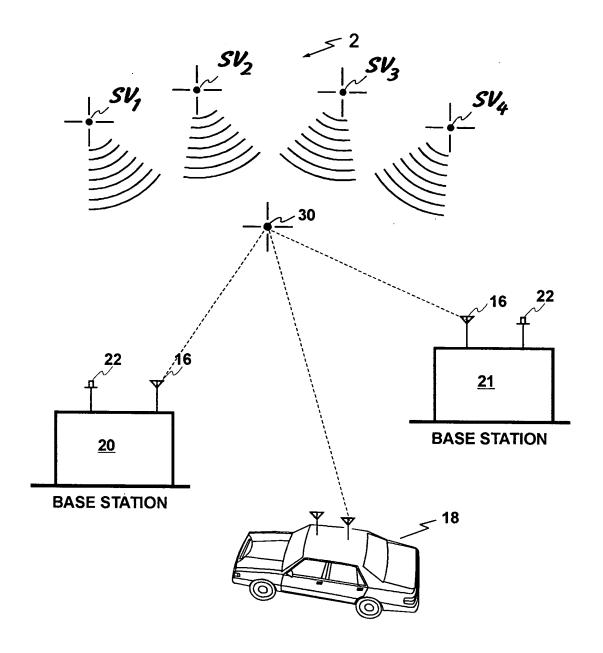


## **Prior Art**

Fig. 1



Prior Art Fig. 2



Prior Art Fig. 3

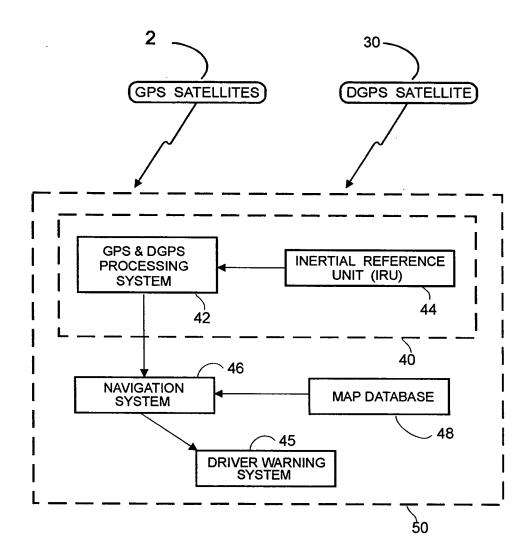


Fig. 4

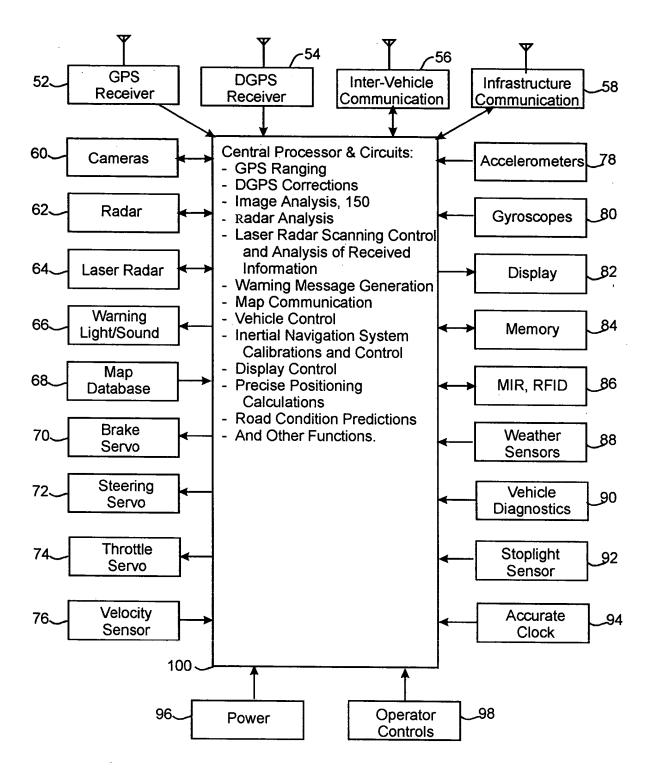


Fig. 5

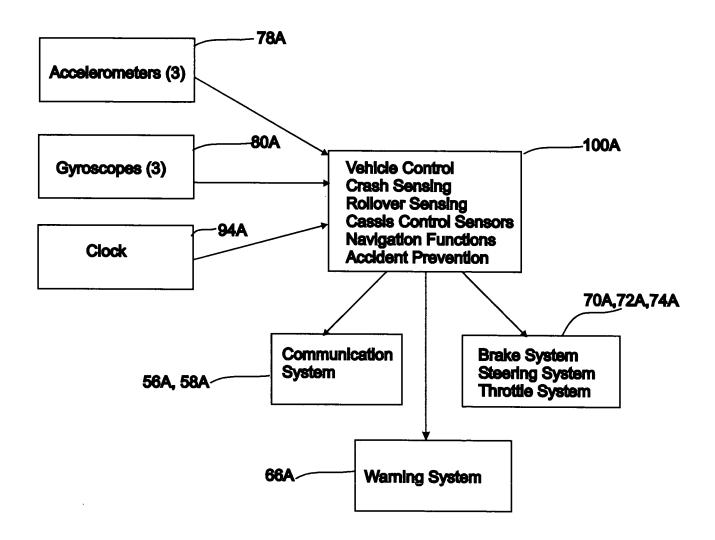


Fig. 5A

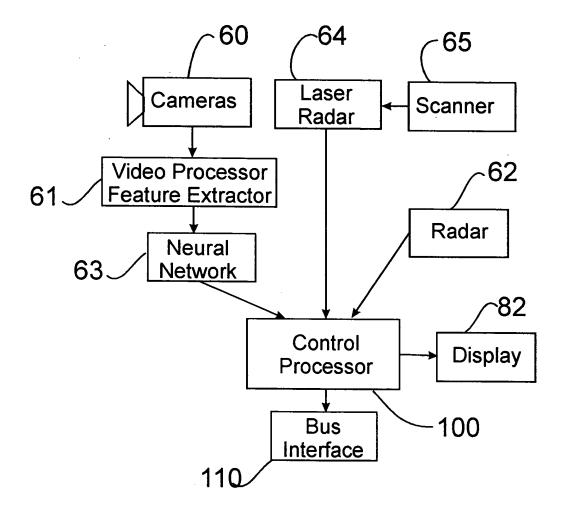


Fig. 6

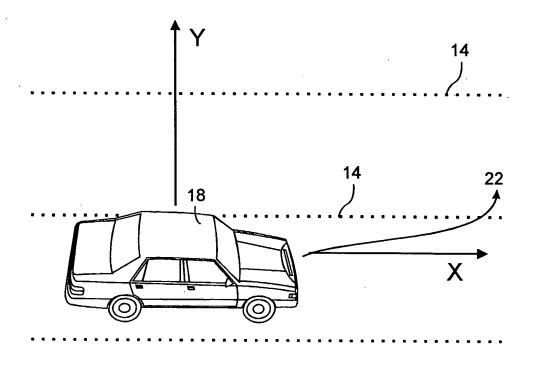


Fig. 7

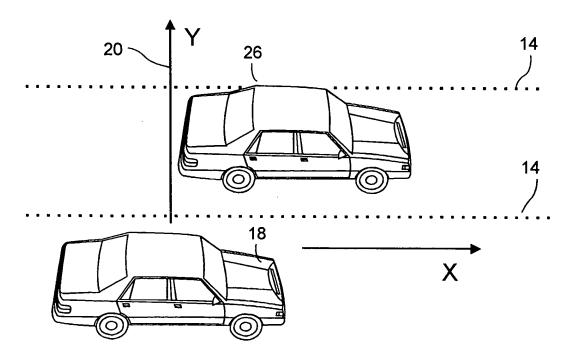


Fig. 8

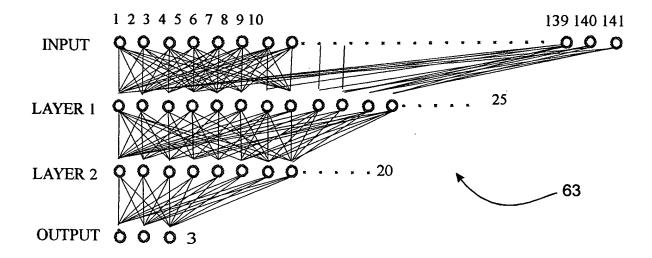


Fig. 9

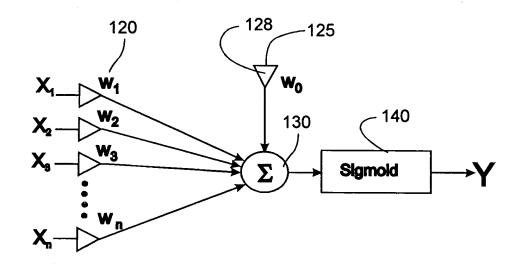
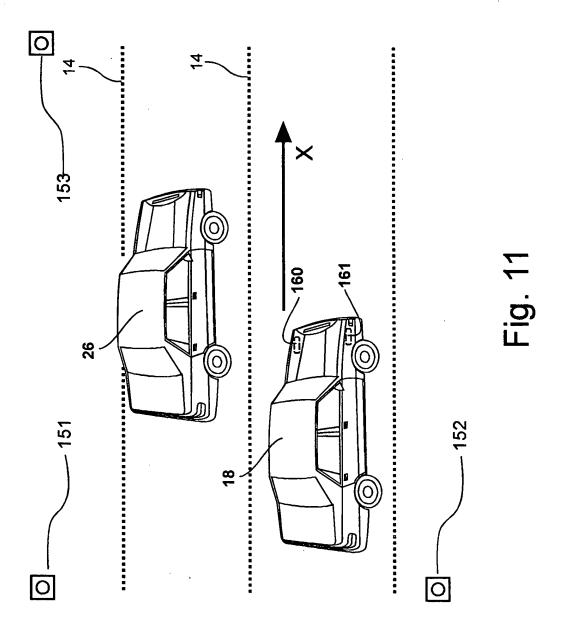
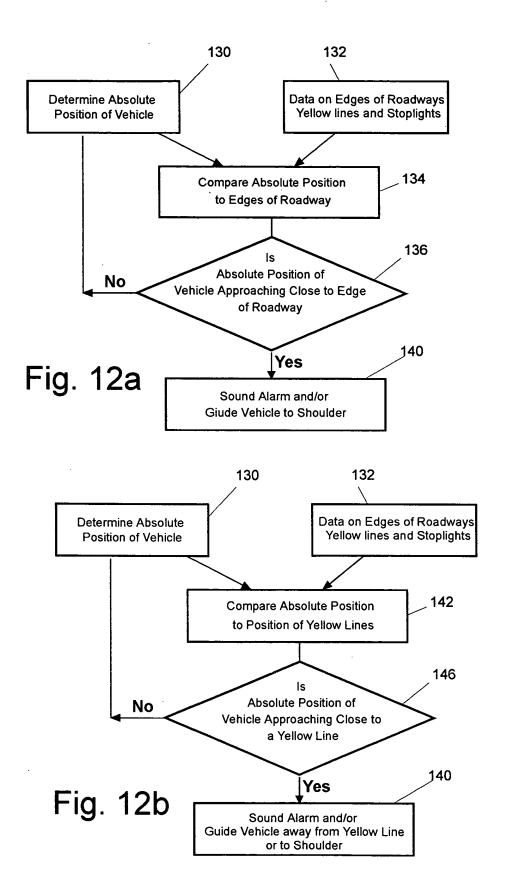
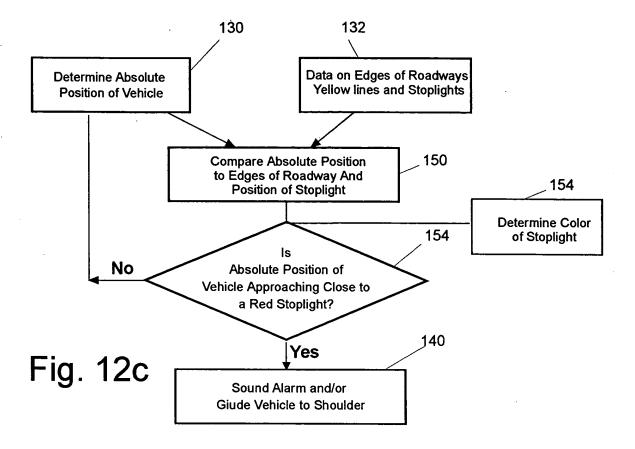


Fig. 10







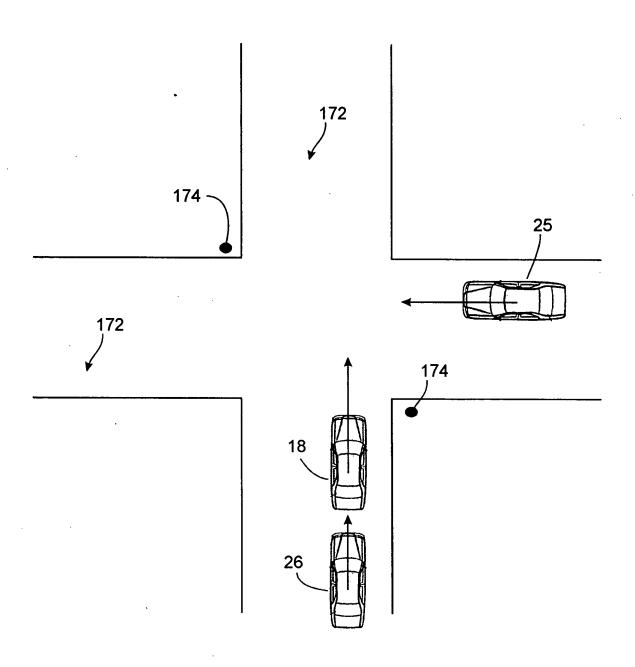
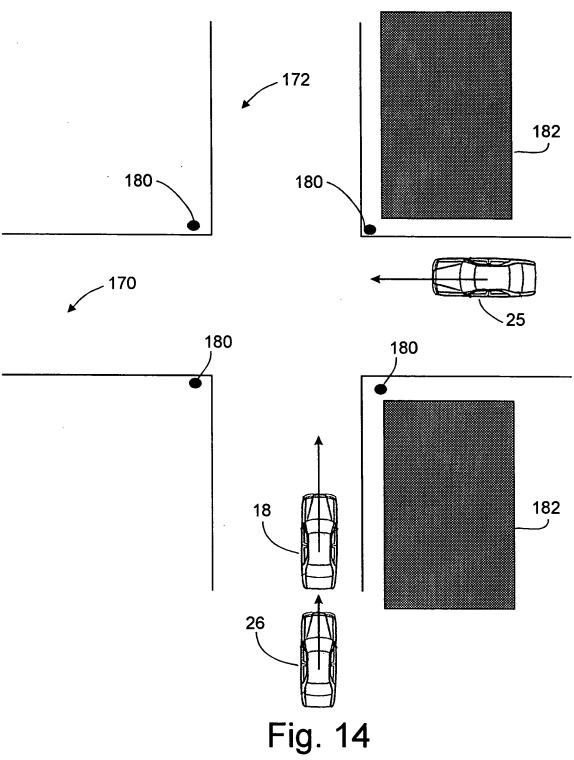


Fig. 13



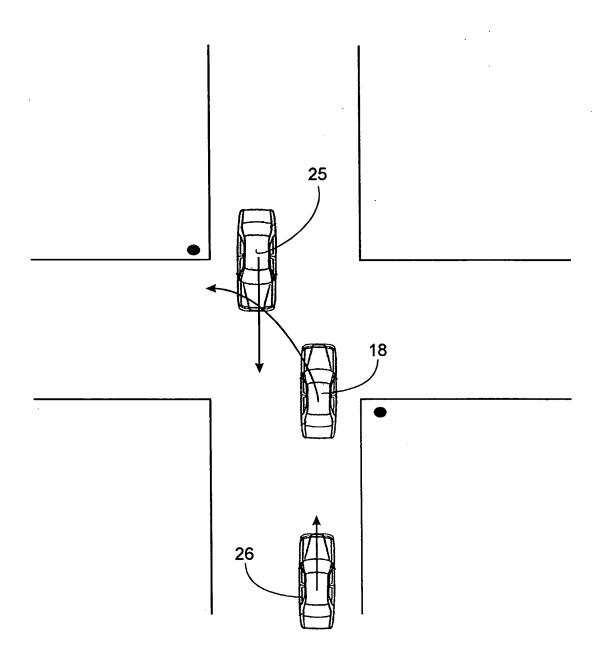


Fig. 15

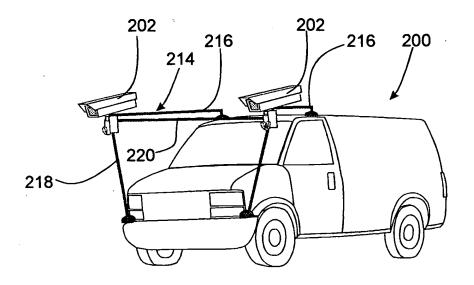


Fig. 16B

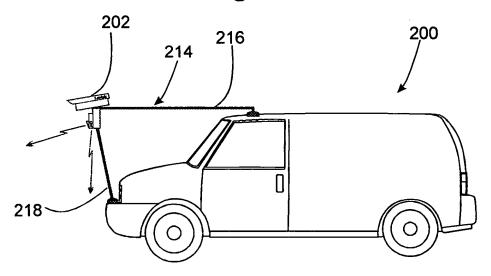


Fig. 16A

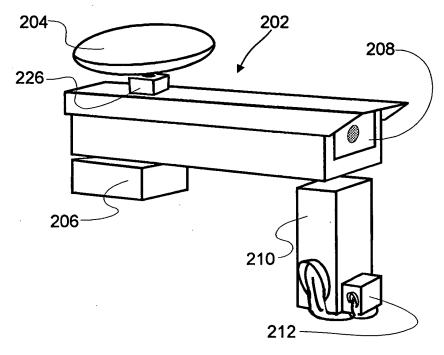


Fig. 17

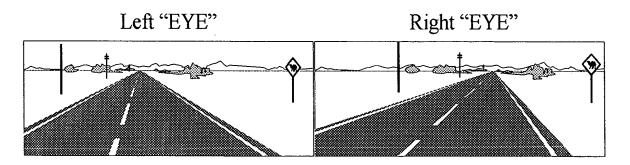
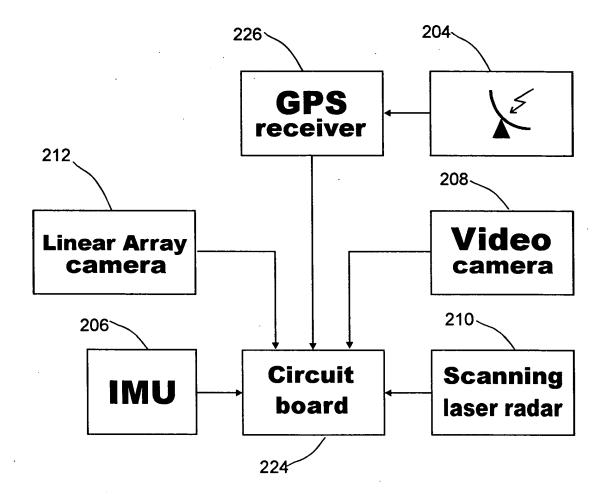


Fig.18



Data acquisition module

Fig. 17A

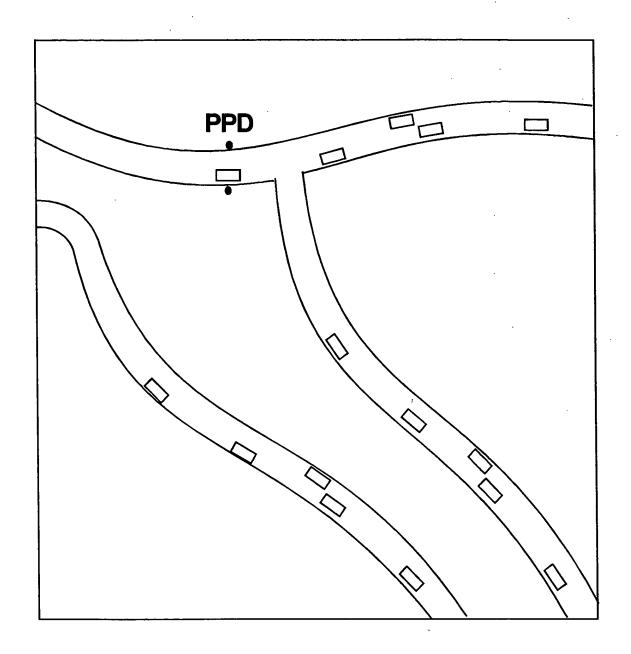


Fig. 19

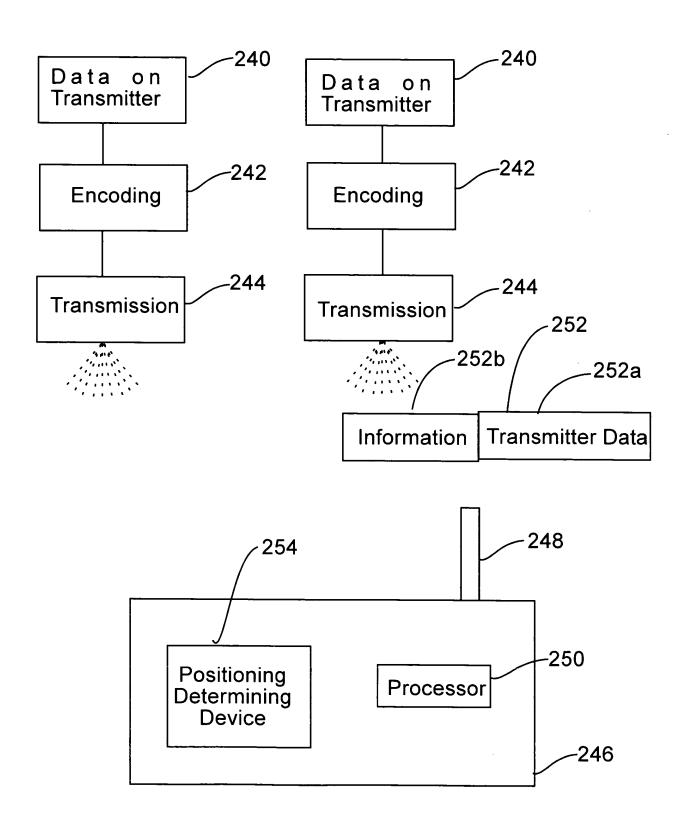


Fig. 20